Abstracts

Single chip 1.9 GHz transceiver frontend MMIC including RX/TX local oscillators and 300 mW power amplifier

J. Kucera and U. Lott. "Single chip 1.9 GHz transceiver frontend MMIC including RX/TX local oscillators and 300 mW power amplifier." 1999 MTT-S International Microwave Symposium Digest 99.4 (1999 Vol. IV [MWSYM]): 1405-1408 vol.4.

A monolithic integrated front end for 1.9 GHz wireless applications has been designed and tested. On a single chip, a low noise amplifier, mixer and local oscillator for the receive path, and a second VCO, buffer and power amplifier for the transmitter have been integrated. A 50 /spl Omega/ SSB noise figure of 1.7 dB with conversion gain of 16.5 dB is achieved for the receiver. The transmit section provides 300 mW output power with >30% overall efficiency.

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